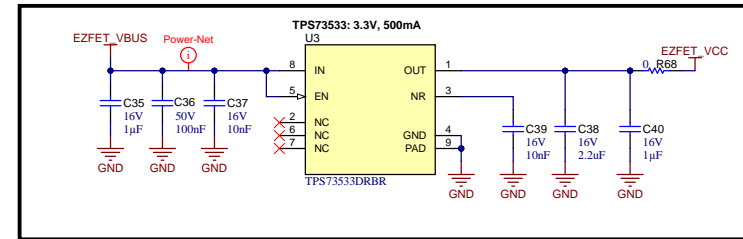
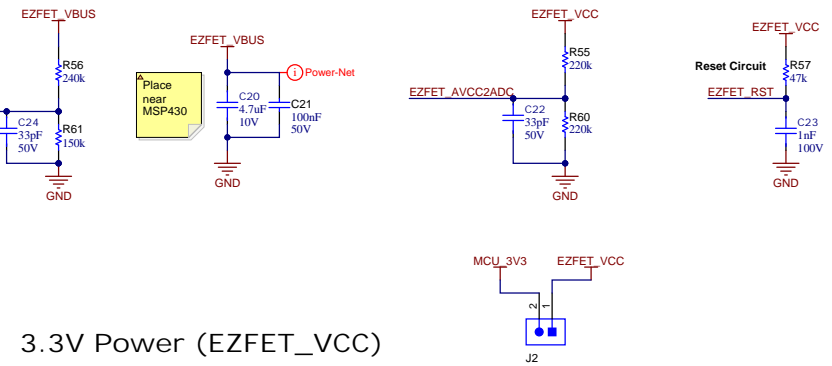
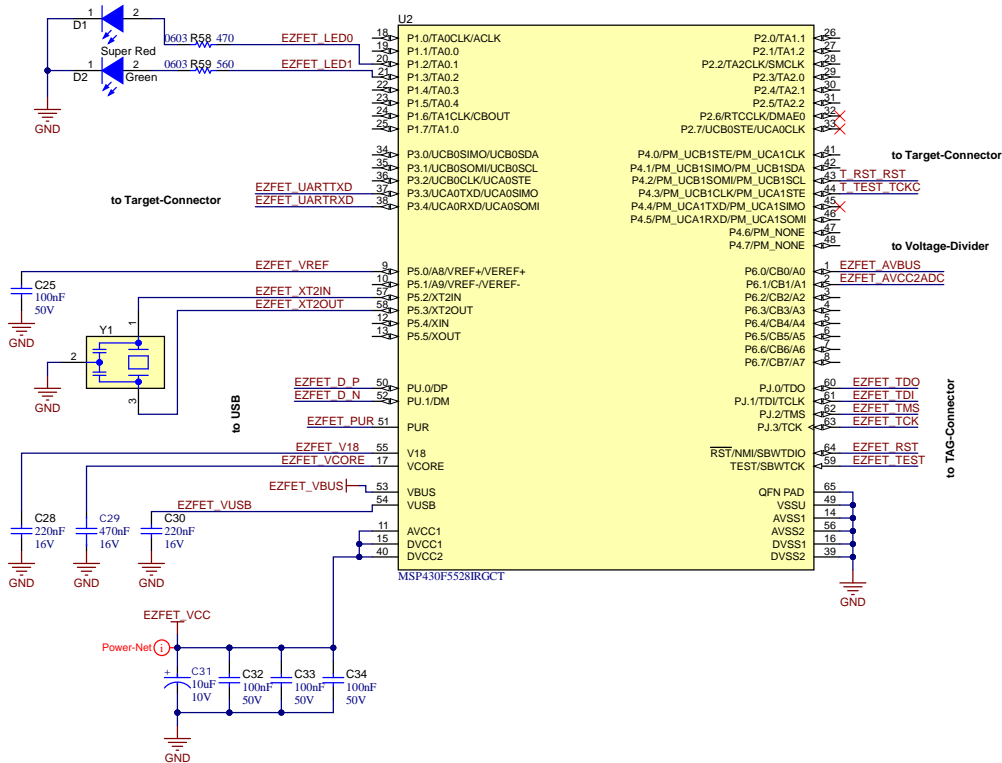
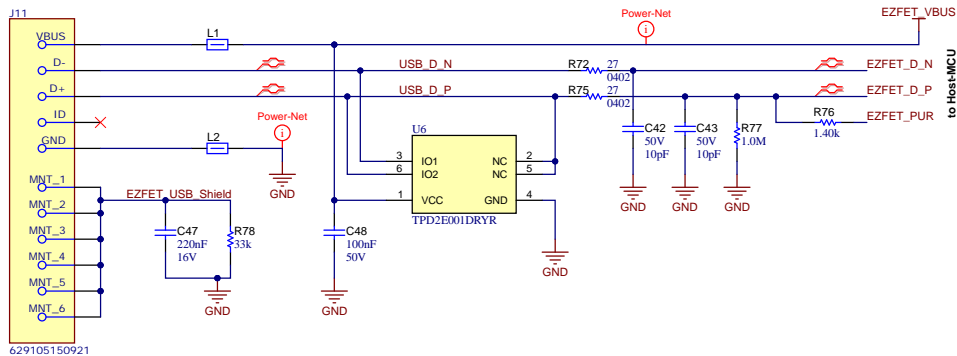


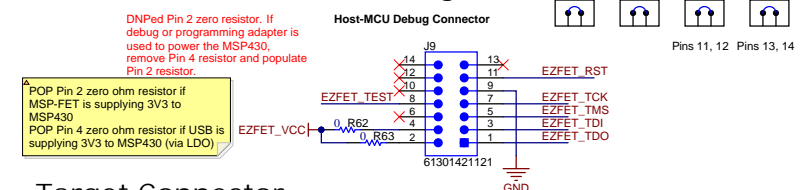
Host MCU for Emulation



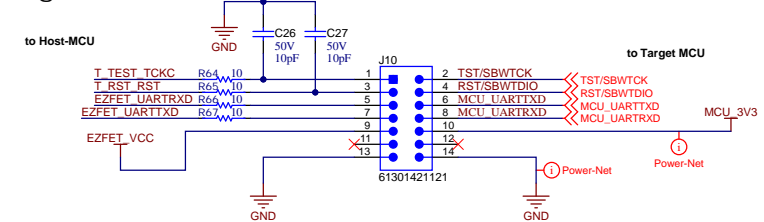
USB-I-Interface



JTAG-Connector (Host Debug)



Target Connector

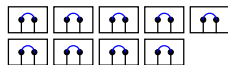


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Orderable:	Designed for: Public Release	Mod. Date: 02-04-2024
TID #: N/A	Project Title: MD075	
Number: MD-075	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 1 of 3
Drawn By: Murugavel Raju	File: MD089E1_ezFET_SchDoc	Size: B
Engineer: Audrey Kushler	Contact: http://www.ti.com/support	http://www.ti.com

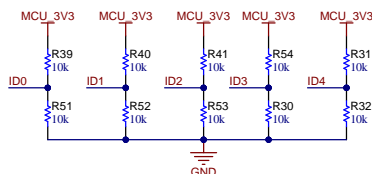


Pin connection diagram for the PC10D04AAN component. The diagram shows a 20-pin connector (TP20 to TP28) connected to a 20-pin component (J7). Pin 1 is connected to IN0, Pin 2 to IN1, Pin 3 to IN0, Pin 4 to IN1, Pin 5 to IN0, Pin 6 to IN1, Pin 7 to nSLEEP, Pin 8 to nSLEEP, Pin 9 to SDO, Pin 10 to SDO, Pin 11 to SDI, Pin 12 to SDI, Pin 13 to SCLK, Pin 14 to SCLK, Pin 15 to nSCS, Pin 16 to nSCS, Pin 17 to R94 (100nF), Pin 18 to R95 (100nF), Pin 19 to R96 (100nF), and Pin 20 to R97 (100nF). The component is labeled PC10D04AAN.



The resistors on the ID[2:0] nets inform the firmware which device ID variant is on this board

Device	ID1
DRV81008-Q1	0
DRV81004-Q1	1
Just In Case	2
Revision 1	3
Revision 2	4



The schematic diagram illustrates the pin connections for the MSP430F5338IPZ microcontroller. The central component is the MSP430F5338IPZ, with pins numbered 1 through 90. The connections are as follows:

- GUI, DRV, FAULT, MCU, and PUSH Buttons:** These buttons are connected to pins 34 through 40. The MCU pin is connected to pin 40.
- TP29:** A test point connected to pin 42.
- MCU_3V3:** A 3V3 supply connected to pin 42.
- TP30 and TP31:** Test points connected to pins 9 and 10.
- TP32, TP33, and TP34:** Test points connected to pins 17, 18, and 19.
- TP35 and TP36:** Test points connected to pins 89 and 90.
- TP37:** A test point connected to pin 90.
- TP38:** A test point connected to pin 90.
- TP39:** A test point connected to pin 90.
- TP40:** A test point connected to pin 90.
- TP41:** A test point connected to pin 90.
- TP42:** A test point connected to pin 90.
- TP43:** A test point connected to pin 90.
- TP44:** A test point connected to pin 90.
- TP45:** A test point connected to pin 90.
- TP46:** A test point connected to pin 90.
- TP47:** A test point connected to pin 90.
- TP48:** A test point connected to pin 90.
- TP49:** A test point connected to pin 90.
- TP50:** A test point connected to pin 90.
- TP51:** A test point connected to pin 90.
- TP52:** A test point connected to pin 90.
- TP53:** A test point connected to pin 90.
- TP54:** A test point connected to pin 90.
- TP55:** A test point connected to pin 90.
- TP56:** A test point connected to pin 90.
- TP57:** A test point connected to pin 90.
- TP58:** A test point connected to pin 90.
- TP59:** A test point connected to pin 90.
- TP60:** A test point connected to pin 90.
- TP61:** A test point connected to pin 90.
- TP62:** A test point connected to pin 90.
- TP63:** A test point connected to pin 90.
- TP64:** A test point connected to pin 90.
- TP65:** A test point connected to pin 90.
- TP66:** A test point connected to pin 90.
- TP67:** A test point connected to pin 90.
- TP68:** A test point connected to pin 90.
- TP69:** A test point connected to pin 90.
- TP70:** A test point connected to pin 90.
- TP71:** A test point connected to pin 90.
- TP72:** A test point connected to pin 90.
- TP73:** A test point connected to pin 90.
- TP74:** A test point connected to pin 90.
- TP75:** A test point connected to pin 90.
- TP76:** A test point connected to pin 90.
- TP77:** A test point connected to pin 90.
- TP78:** A test point connected to pin 90.
- TP79:** A test point connected to pin 90.
- TP80:** A test point connected to pin 90.
- TP81:** A test point connected to pin 90.
- TP82:** A test point connected to pin 90.
- TP83:** A test point connected to pin 90.
- TP84:** A test point connected to pin 90.
- TP85:** A test point connected to pin 90.
- TP86:** A test point connected to pin 90.
- TP87:** A test point connected to pin 90.
- TP88:** A test point connected to pin 90.
- TP89:** A test point connected to pin 90.
- TP90:** A test point connected to pin 90.

Orderable:	Designed for: Public Release	Mod. Date: 24-04-2024
TID #: N/A	Project Title: MD075	
Number: MD-075	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By: Tilden Chen	File: MD089E1 MCU SchDoc	Size: B
Engineer: Audrey Kuehler	Contact: http://www.ti.com/support	

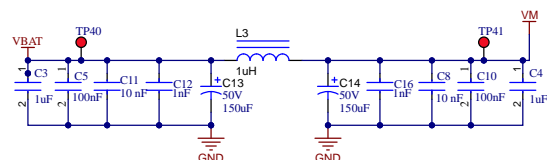
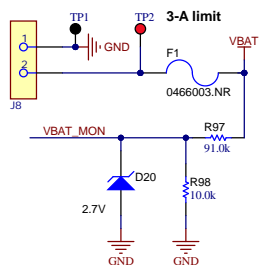


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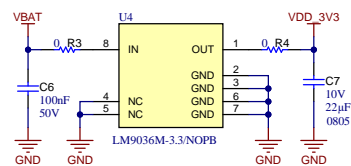
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4.5 - 40V DC

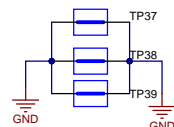
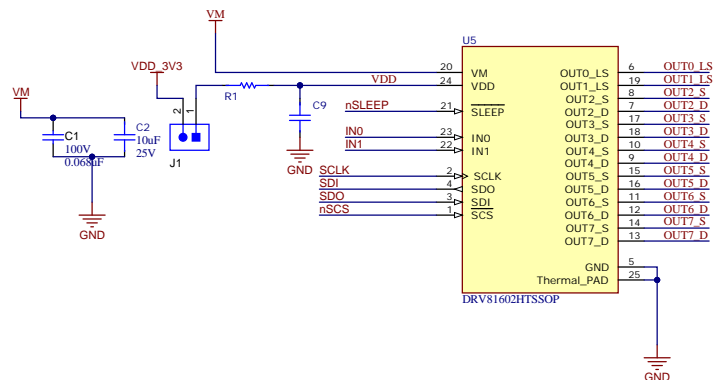
Board power



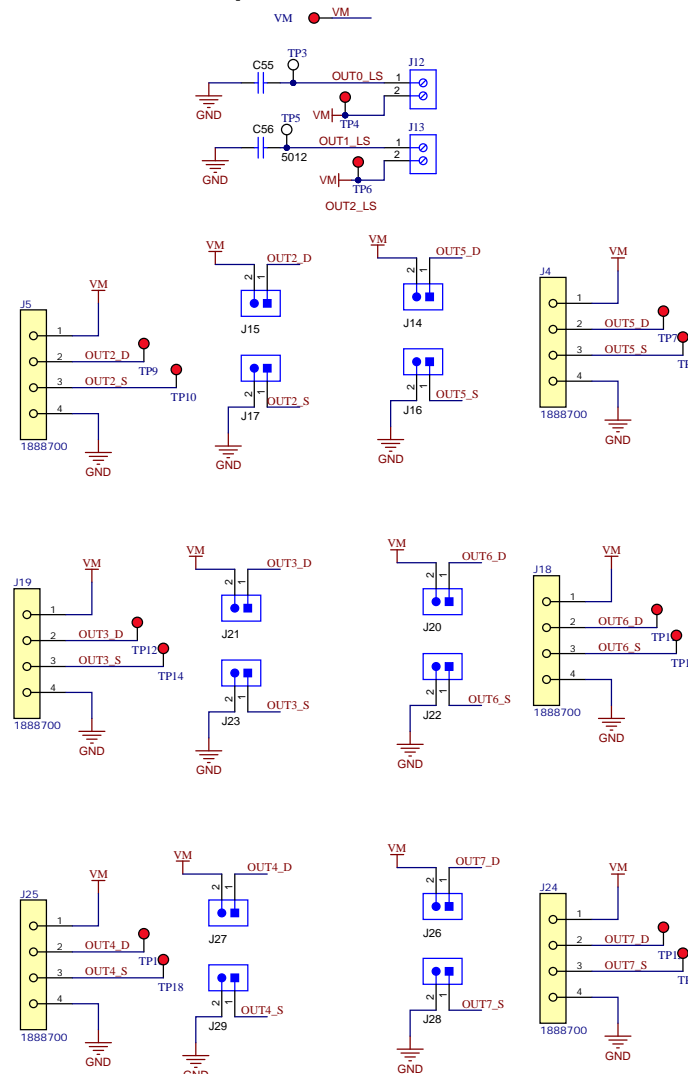
3.3V LDO



DRV81602-Q1



Output Connector



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TID #: N/A	Project Title: MD075	
Number: MD-075	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 2 of 3
Drawn By: Tilden Chen	File: MD089E1 - DRV81xxx.SchDoc	Size: B
Engineer: Audrey Kuehler	Contact: http://www.ti.com/support	http://www.ti.com

